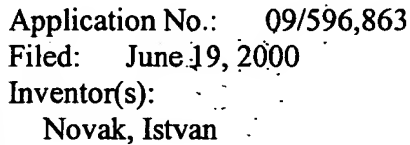


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Title: BYPASS CAPACITOR METHODS FOR ACHIEVING A DESIRED VALUE OF ELECTRICAL IMPEDANCE BETWEEN PARALLEL PLANAR CONDUCTORS OF AN ELECTRICAL POWER DISTRIBUTION STRUCTURE AND ASSOCIATED ELECTRICAL POWER DISTRIBUTION STRUCTURES

Examiner: Bettendorf, J.
Group/Art Unit: 2817
Atty. Dkt. No: 5181-62800

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This paper is submitted in response to the Office Action of October 10, 2001, to further highlight why the application is in condition for allowance.

IN THE DRAWINGS:

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